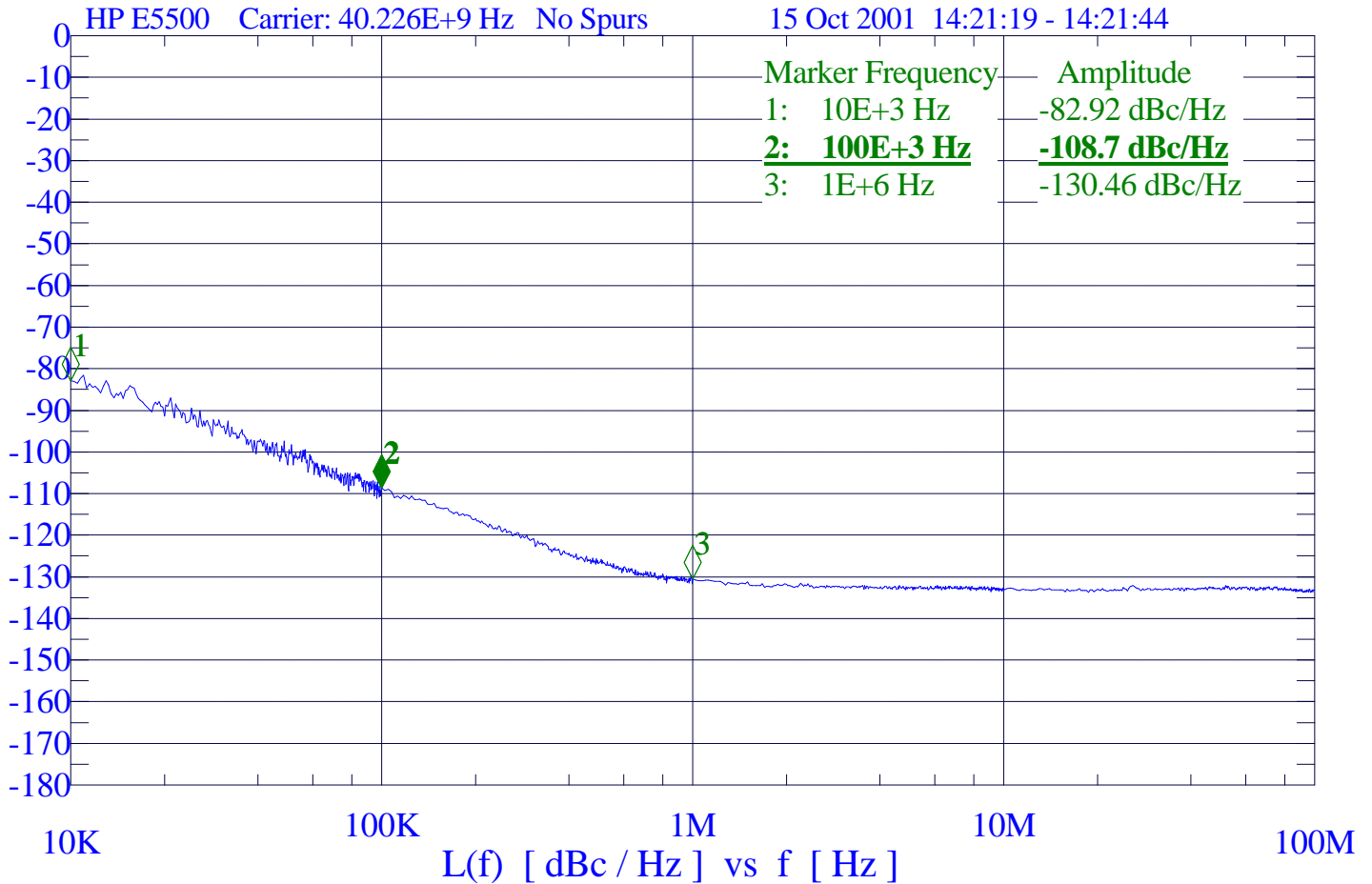


# 40 GHz FRDRO



## 40 GHz FRDRO

Measurement time: 15 Oct 2001 14:21:19 - 14:21:44

Measurement type: Absolute phase noise (using a phase locked loop)

Start offset frequency: 10E+3 Hz

Stop offset frequency: 100E+6 Hz

Minimum number of FFT averages: 4

Carrier Source frequency: 40.226E+9 Hz

Detector input frequency: 626E+6 Hz

Detector: Automatic detector selection

Nominal VCO tune constant: 800E+3 Hz/Volt

VCO center voltage: 0 Volts

VCO tune range: 5 Volts

Detector constant cal method: Derive from measured beatnote.

Detector constant: 204E-3 V/Rad

VCO tune constant cal method: Measure the Tune Constant.

Current VCO tune constant: 584.6E+3 Hz/Volt

PLL Integrator attenuation: 0 dB

Phase Locked Loop suppression was NOT verified.

Closed PLL BW: 12.368E+3 Hz

Peak Tune Range: 2.466E+6 Hz

Assumed Pole: 300E+3 Hz

Carrier Source: (manual)

Reference Source: HP-8663

Time Base: (none)

Downconverter: HP-70427 ; VCO tuned using DC FM.

LNA gain: 42 dB

Software Version: A.01.04